

10/60/80

ISSUE CLASSIFICATION	
Class	Subclass

PATENT

U.S. UTILITY Patent Application

O.I.P.E. PATENT DATE
 A IK4 Su
 SCANNED Q.A.

APPLICATION NO. 09/927011	CONT/PRIOR	CLASS 224 214	SUBCLASS 7/18	ART UNIT 2556 2133	EXAMINER Decade
------------------------------	------------	--------------------------------	------------------	-------------------------------------	--------------------

APPLICANTS

Thomas Bartenstein
L. Farnsworth
Douglas Heaberlin
Edward Horton

Internal cache for on chip test data storage

TITLE

PTO-2040
12/89[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ _____ (Primary Examiner) (Date)			ISSUE FEE	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal ____months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)			ISSUE BATCH NUMBER	
WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.					

Form **PTO-436A**
(Rev. 6/99)

FILED WITH: ☐ DISK (CRF) ☐ FICHE ☐ CD-ROM
(Attached in pocket on right inside flap)

BEST AVAILABLE COPY

(FACE)